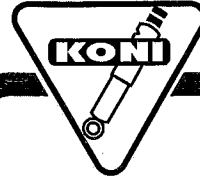
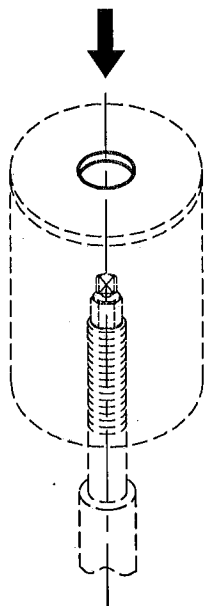
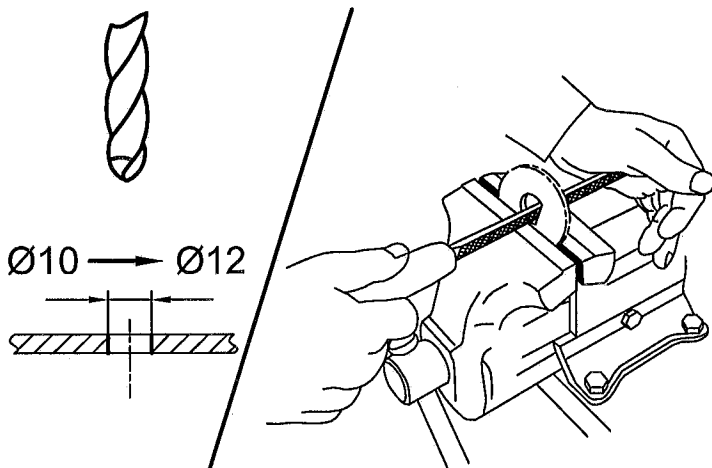
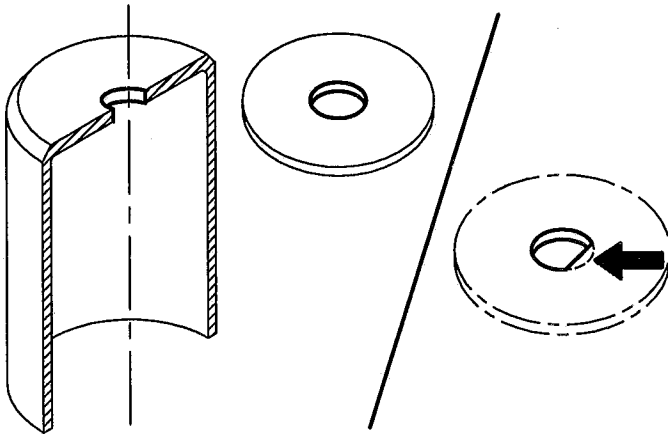


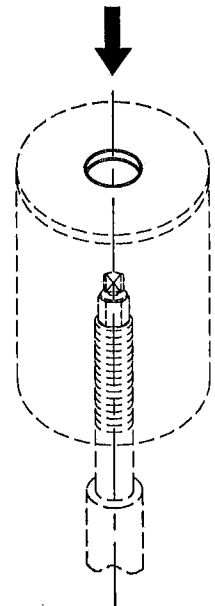
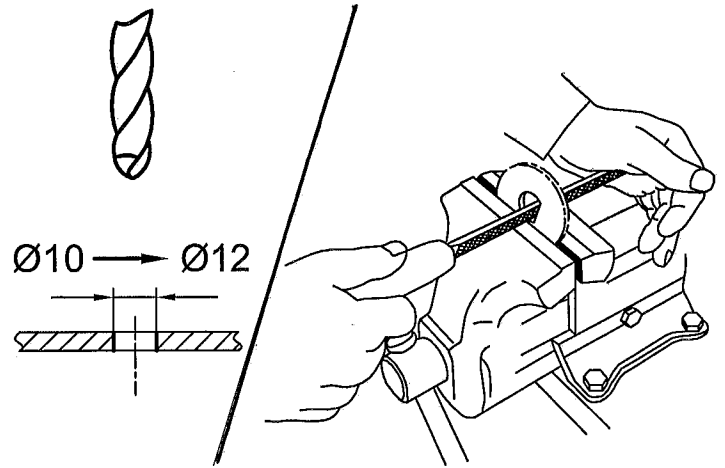
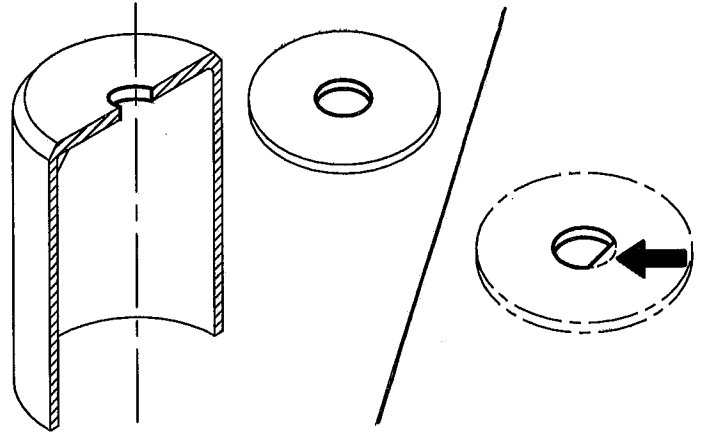
instruction

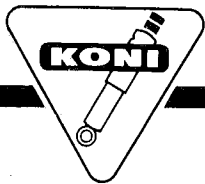
1027 05 15 14
206



instruction

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206

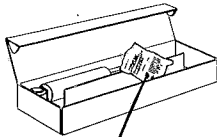
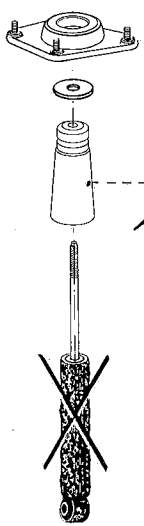




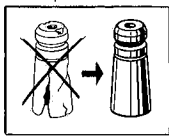
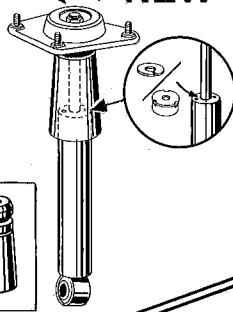
instruction

1027 05 18 76
301/2

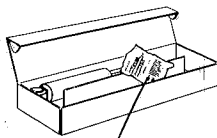
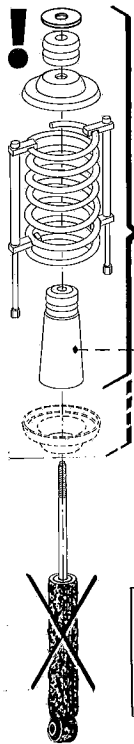
ORIGINAL



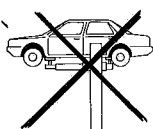
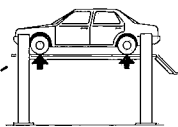
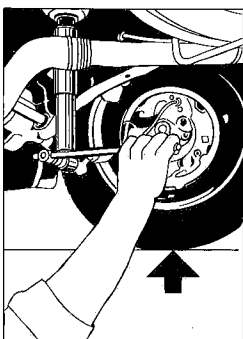
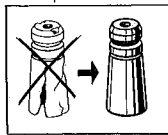
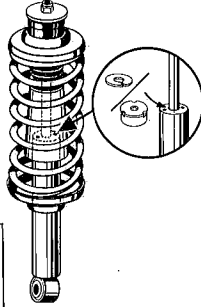
NEW



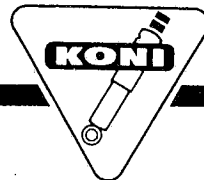
ORIGINAL



NEW



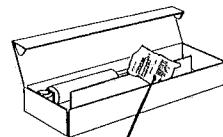
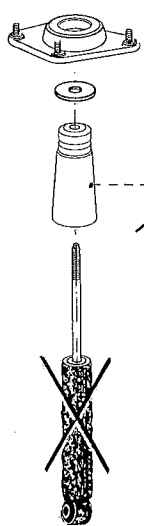
AFST. / 1A →



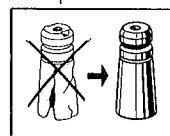
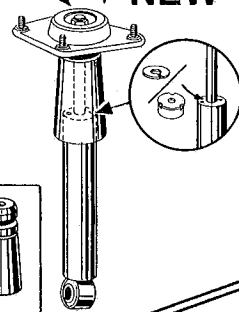
instruction

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301/2

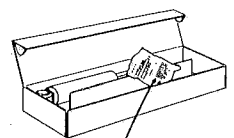
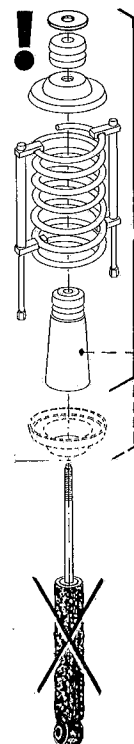
ORIGINAL



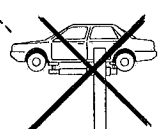
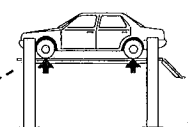
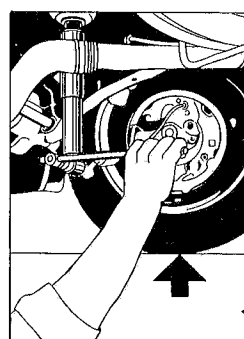
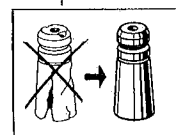
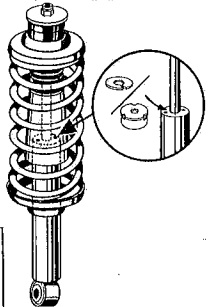
NEW



ORIGINAL



NEW



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